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## **ABHINANDAN MAJUMDAR**

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### **Education**

- Master of Science in Computer Engineering at Columbia University (Graduation Date: December 2008), (CGPA: 3.945 / 4).
- Bachelor of Engineering in Computer Engineering at National Institute of Technology Karnataka, Surathkal, in June 2006 (Aggregate: 81%).
- 12<sup>th</sup> CBSE Board 2002 from DAV Public School, Bilaspur, INDIA (Agg. 90.4%).

### **Projects Done/Undertaken**

- I/O Placement and Floorplanning for a Router – A Course Project for Topics in Electronic Circuits, under Prof. Ken Shepard at Columbia University.
- AES Decryption on FPGA – A Course Project for Embedded Systems Design, under Prof. Stephen Edwards at Columbia University.
- Vulnerability of On-chip Interconnection Networks to Soft Errors – under Prof. Simha Sethumadhavan and Luca Carloni, at Columbia University.
- Full Custom 4KB SRAM Design – A Course Project for VLSI Circuits at Columbia University.
- Storage Driver/Tools development for Intel® Turbo Memory Module at Intel Technology India Pvt. Ltd., Bangalore.
- “FPGA Implementation of Integer Linear Programming Accelerator”, under Prof. M. Balakrishnan, CSE Dept., IIT Delhi, during June-July 2005.

### **Work Experience**

- Worked as Software Engineer at Intel Technology India Private Limited.
- Worked as Summer-Intern at Summer Workshop in Digital Systems Design – 2005 at Indian Institute of Technology – Delhi.

### **Publications**

- “FPGA IMPLEMENTATION OF INTEGER-LINEAR PROGRAMMING ACCELERATOR”, published at ICSCI-2006.

### **Skill Set**

- Languages: C, JAVA, WIN32, TASM, Shell, FLEX, YACC, MATLAB, SKILL.
- Design Languages: VHDL, VB, VC++, Cadence, HSPICE, SPECTRE
- Database Languages: MS-Access, ORACLE, SQL

### **Awards/distinction received**

- Received Full MS Teaching Assistant Fellowship for Spring 2008, from CS Dept., Columbia University.
- Received SPONTANEOUS RECOGNITION AWARD at Intel Tech. Bangalore.
- Paper titled “FPGA IMPLEMENTATION OF INTEGER LINEAR PROGRAMMING ACCELERATOR”, received “Best Paper Submission” at ICSCI-2006.