CHRISTOS VEZYRTZIS

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Education

- **Columbia University, Fu Foundation School of Engineering & Applied Science, New York, NY**
  PhD candidate, Electrical Engineering Jan. 2007 – present
  Advisors: Prof. Yannis Tsividis and Prof. Steven Nowick (CS dept.)
  GPA: 4.0/4.0

- **Columbia University, Fu Foundation School of Engineering & Applied Science, New York, NY**
  MSc in Electrical Engineering with concentration on Circuits & Electronics Sep. 2006 – Dec.2007
  GPA: 4.0/4.0

  Courses: Adv. communication circuits, Advanced analog IC, MOS Transistors, Microwave circuits, Analog systems in VLSI, Digital Signal Processing, Embedded systems design, Advanced logic design, VLSI testing.

  Completed projects: Receiver front-end design, Microwave LNA design, Pipeline ADC design, MPEG codec implementation on FPGA, Design and simulation of a floating-point adder in VHDL.

- **National Technical University of Athens, Athens, Greece** Sep. 2001- July 2006
  Diploma in Electrical and Computer Engineering
  GPA: 8.94/10.00

Work & Teaching Experience

- **Columbia University, Fu Foundation School of Engineering & Applied Science, New York, NY**
  Research assistant Jan. 2008 – present
  Completed research projects
  - Ultra-low-voltage reference design (under supervision of Prof. Peter Kinget)
  - Processing of signals using level-crossing sampling
  - Direct processing of MPEG-encoding signals using companding

  Teaching assistant Jan. 2008 – Dec. 2008
  MOS Transistors, Random signals and noise
- **National Technical University of Athens** Summer 2009
  - Design of complete RF measurements loop
- **Conexant Systems** Summer 2007
  - Design of mixed signal PLLs involving Bang-Bang structures.

Publications

- C. Vezyrtzis and Y.Tsividis, Processing of signals using level-crossing sampling, Proc. ISCAS 2009, Taipei, Taiwan, May 2009
- P. Kinget, C. Vezyrtzis et. al., "Voltage References for Ultra-Low Supply Voltages," *IEEE Custom Integrated Circuits Conference*, pp.715-720, 2008.

Research Interests

VLSI circuit design, Digital Signal Processing (DSP), Asynchronous circuit design.

Fellowships – Awards - Memberships

- Awarded the Gerondelis Foundation scholarship.
- Awarded the Propondis Foundation scholarship, among top 30 applicants, for Graduate Studies
- Awards for performance in national mathematics competitions (Eyklides, Archimedes) (2000,2001)
- Member, IEEE.

Skills

Language skills:	Greek (mother language), English (fluent), German (fair)
IC Design, tools and prog. languages :	Cadence, MATLAB (Instrumentation, Simulink), ADS, VHDL, C, Java and LaTex

Website

http://www.columbia.edu/~cv2176/home.html